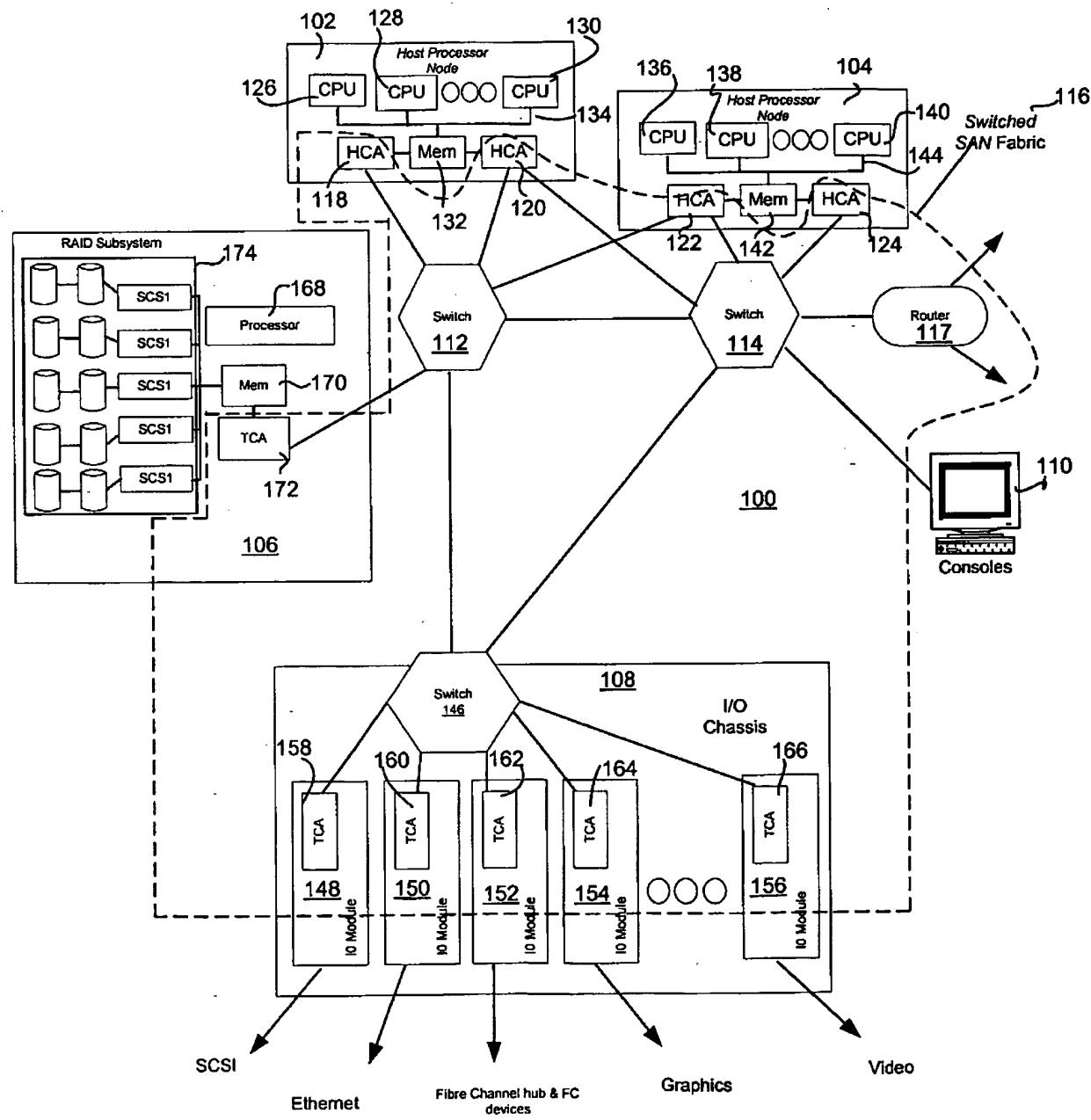


EF40965243305

Figure 1

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AUS9-2000-0640-US1



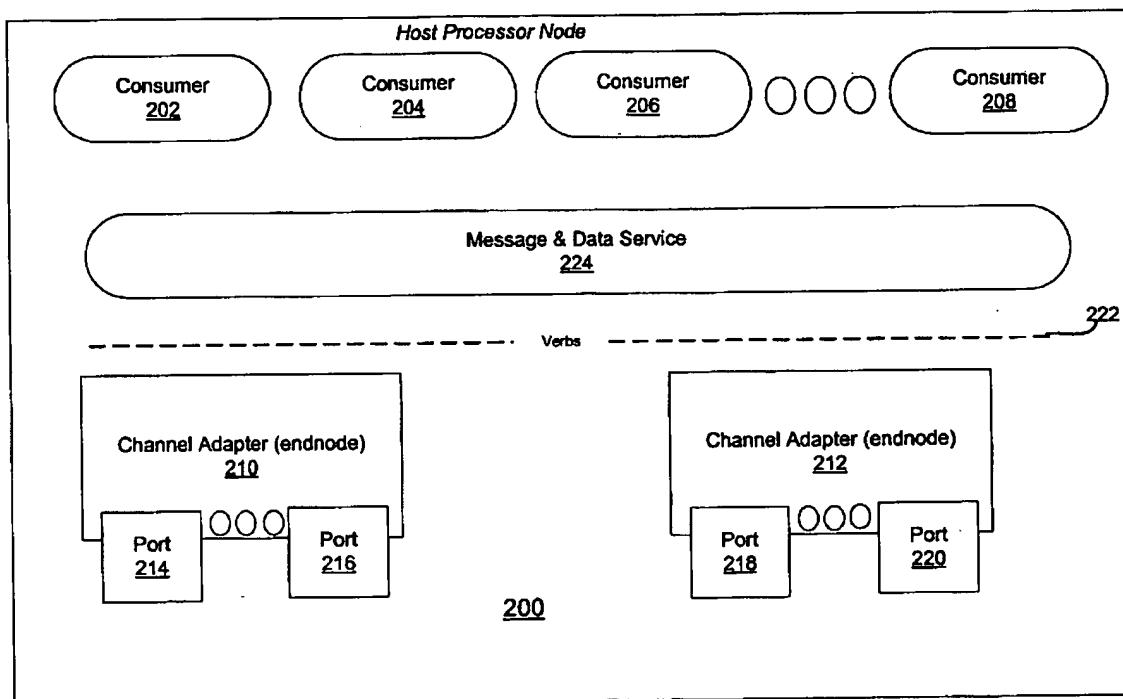


Figure 2

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AUS8-2000-0640-US1

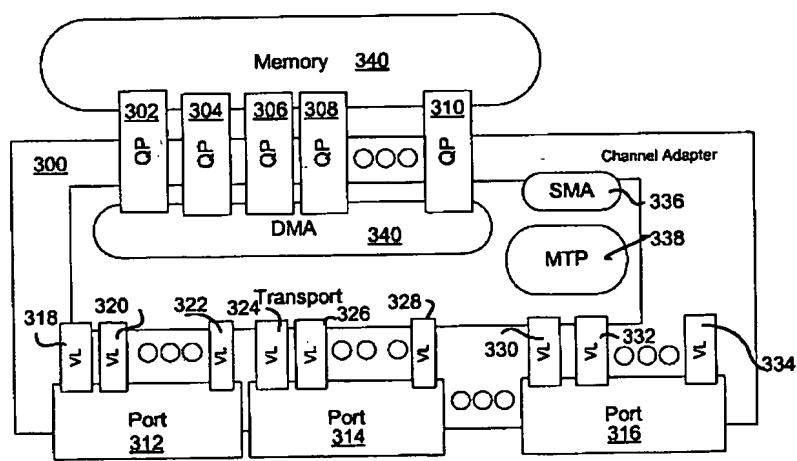


Figure 3

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Figure 4

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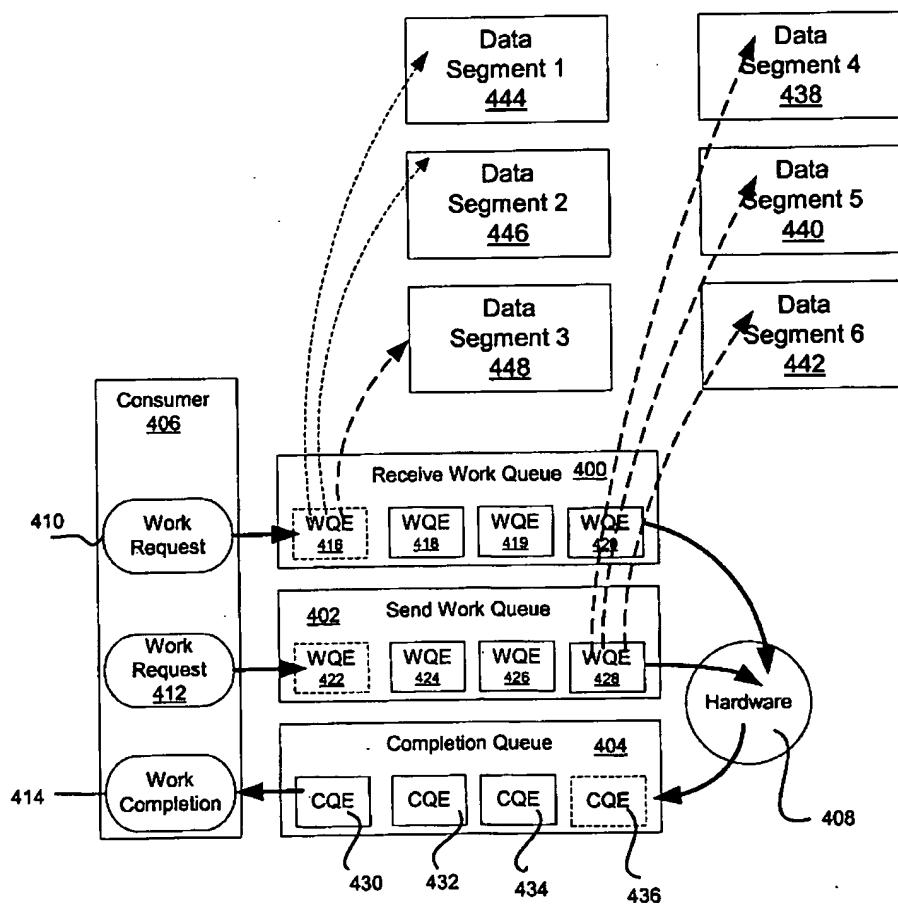
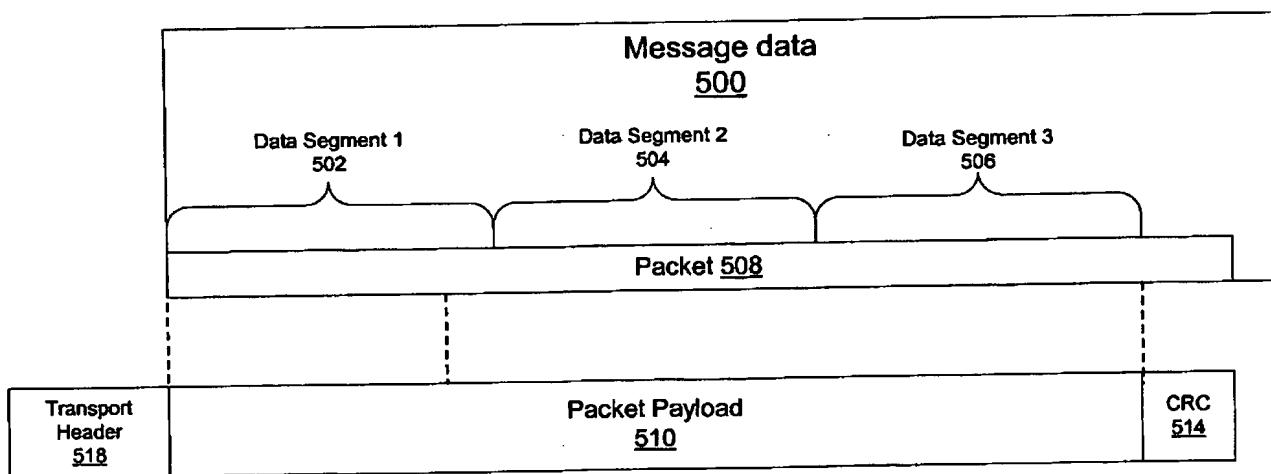


Figure 5

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Figure 6

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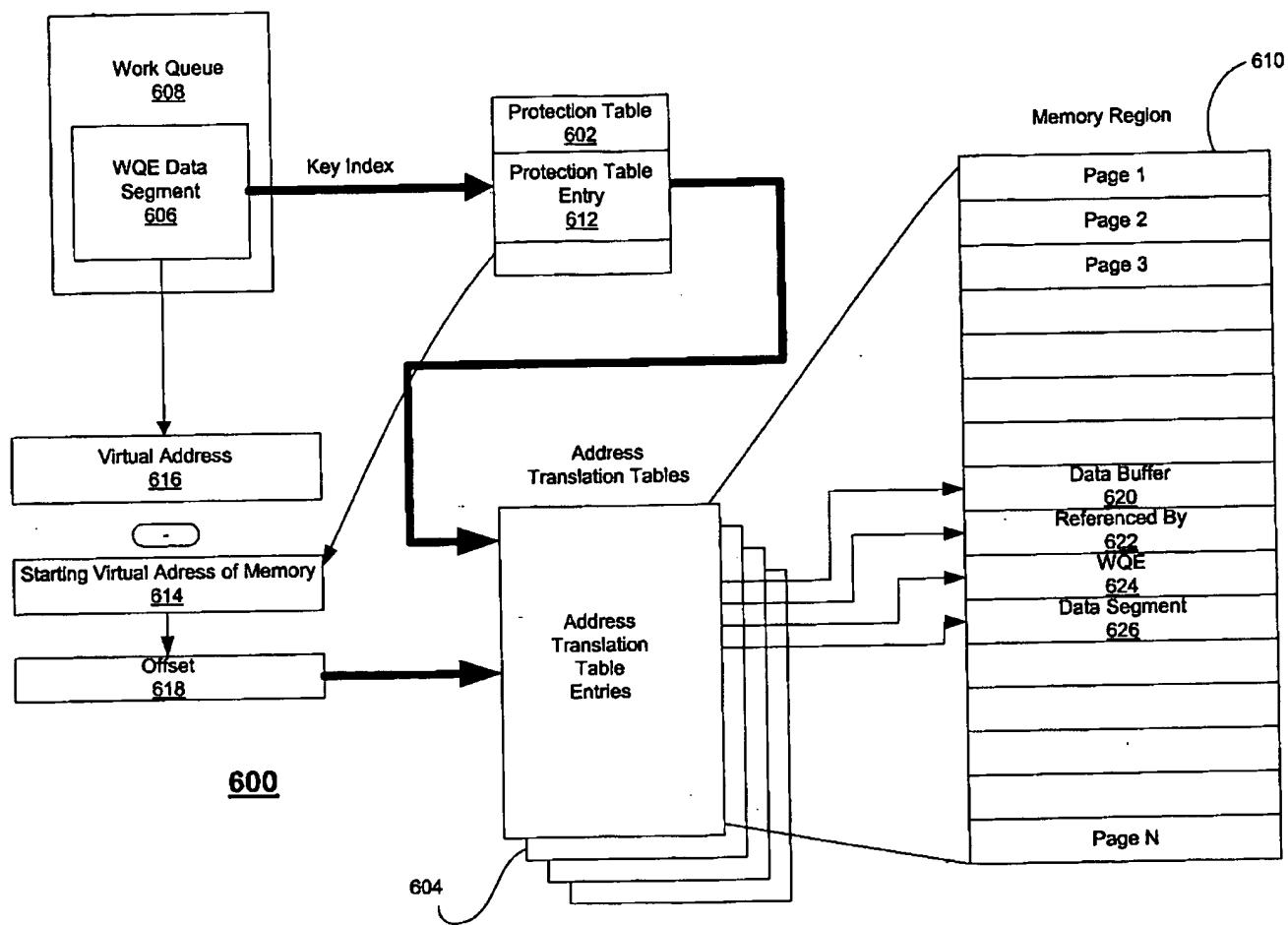


Figure 7

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AUS9-2000-0840-US1

The diagram illustrates the structure of a Protection Table Entry. It consists of a vertical stack of seven horizontal rows, each representing a field in the entry. A curved arrow labeled '700' points to the top row. The fields and their corresponding memory addresses are:

Virtual Address of Start of Memory Region	<u>702</u>
Length of Memory Region	<u>704</u>
Protection Domain	<u>706</u>
Local and Remote Access Control	<u>708</u>
Key_Instance	<u>710</u>
Address Translation Pointer	<u>712</u>

Figure 8

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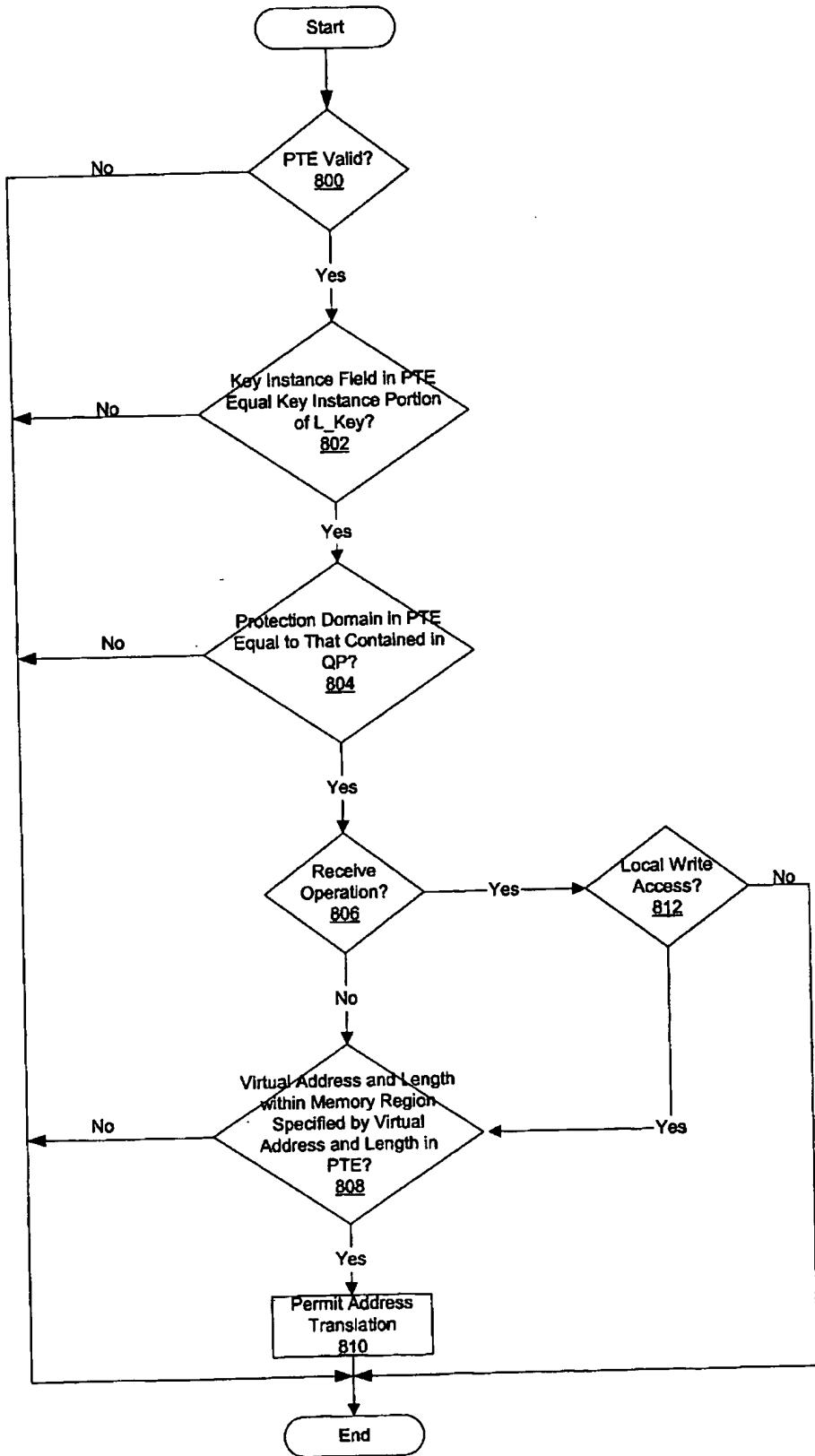


Figure 9

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